

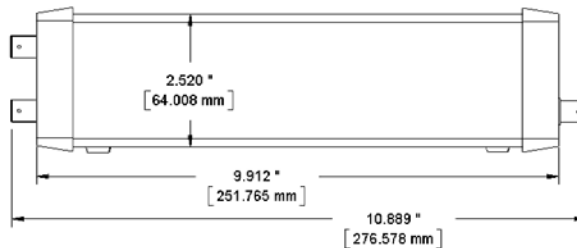
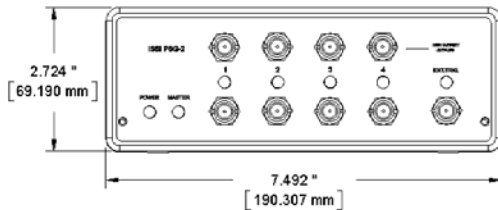
PSG-2 Ethernet based Pulse/Delay Generator

(Product ID: PSG-2)

The PSG-2 is a four-channel pulse/delay generator. It can be used as a master clock for timing and delay control. It can be externally triggered or gated by an external event. Pulse width, delay, repetition rate, and other timing parameters can be controlled by one of two available interfaces



- Two dual-channel independently programmable pulse generators
 - PSG-2 can produce two separate frequency outputs either synchronously or asynchronously.
- Four high-current TTL level outputs
 - Extends the utility of this generator by allowing it to drive low impedance loads (50 Ohms) such as solenoids and drive logic over long cable runs
- Remotely programmable through Ethernet
- Multiple timing bases (s, ms, μ s, ns)
- Multiple PSG-2s can be cascaded to increase the number of channels
- BNC inhibit input (TTL high level) to allow the PSG-2 to be gated by an external source
- Firmware upgrade through USB



SPECIFICATIONS

Input Power	85-264V, 50-60Hz	Output Voltage	5V TTL
Interface	Ethernet, USB 2.0	Divide-by	2-255
Counter Timers	2	Clock Speed	20-ns, 40-ns, 80-ns, 160-ns
Pulse Width	20 ns to 10 s	Inhibit	TTL High
Repetition Rate	0.1 Hz to 20 kHz	Burst Mode	user defined
Trigger Threshold	0.05-5V	ECCN	EAR99